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... the performance of coordinated checkpointers on networks of workstations using RAID techniques - all 12 versions »

JS Plank - 15th Symposium on Reliable Distributed Systems, 1996 - doi.ieee.org

... ck e pointing is a well-known **technique** for providing ... These approaches apply RAID **techniques** [5] to standard coordinated ... of the checkpoint is made **using** copy-on ...

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A tutorial on Reed-Solomon coding for fault-tolerance in RAID-like systems - all 20 versions »

JS Plank - Software Practice and Experience, 1997 - doi.wiley.com

... 13 Several **techniques** ... Figure 1 shows an example configuration **using** this **technique** (which we henceforth call 'RS-Raid') for n = 8 and m = 2. The ...

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RAID-II: a high-bandwidth network file server - all 6 versions »

AL Drapeau, KW Shirriff, JH Hartman, EL Miller, S ... - Computer Architecture, 1994.

Proceedings the 21st Annual ..., 1994 - ieeexplore.ieee.org

... Any client request can be ser- viced **using** either access mode, but we ... or more XBUS controller boards (contained in the center rack of RAID-II) over the VME ...

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Performance Evaluation of Distributed iSCSI RAID - all 4 versions »

X He, P Beedanagari, D Zhou - ... Workshop on Storage Network Architecture and Parallel I/Os, 2003 - iweb.tnitech.edu

... storage targets similar to RAID by **using** striping and rotated parity **techniques** ... in iRAID leads to performance gain while **using** the RAID parity **technique** ...

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Applied techniques for high bandwidth data transfers across wide area networks - all 29 versions »

J Lee, D Gunter, B Tierney, B Allcock, J Bester, J ... - Proceedings of Computers in High Energy Physics, 2001 - liris.cnrs.fr

... For example, a RAID system may stripe across the ... in size and were arranged **using** the partitioned ... implementations here show not only how to use these **techniques** ...

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Improving Goodput by Coscheduling CPU and Network Capacity - all 7 versions »

J Basney, M Livny - International Journal of High Performance Computing ..., 1999 - hpc.sagepub.com

... For each **technique**, we first consider the impact ... there are a number of **techniques** we believe ... checkpointers on networks of workstations **using** raid tech- niques. ...

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1 [Reliability and performance of hierarchical RAID with multiple controllers](#)

Sung Hoon Baek, Bong Wan Kim, Eui Joung Joung, Chong Won Park

August 2001 **Proceedings of the twentieth annual ACM symposium on Principles of distributed computing PODC '01**

Publisher: ACM Press

Full text available: [pdf\(663.16 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Redundant arrays of inexpensive disks (RAID) offer fault tolerance against disk failures. However a storage system having more disks suffers from less reliability and performance. A RAID architecture tolerating multiple disk failures shows severe performance degradation in comparison to the RAID Level 5 due to the complexity of implementation. We present a new RAID architecture that tolerates at least three disk failures and offers similar throughout to the RAID Level 5. We call it the hierar ...

Keywords: Markov process, hierarchical RAID, high reliability, three-failure-tolerant array

2 [Hierarchical disk cache management in RAID 5 controller](#)

Jung-ho Huh, Tae-mu Chang

December 2003 **Journal of Computing Sciences in Colleges**, Volume 19 Issue 2

Publisher: Consortium for Computing Sciences in Colleges

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In RAID system, disk cache is one of the important elements in improving the system performance. Two-level cache displays superior performance in comparison to single cache and is effective in temporal and spatial locality. The proposed cache system consists in two levels. The first level cache is a set associative cache with small block size whereas the second level cache is a fully associative cache with large block size. In this paper, a RAID 5 disk cache model is presented that is located on ...

3 [The HP AutoRAID hierarchical storage system](#)

John Wilkes, Richard Golding, Carl Staelin, Tim Sullivan

February 1996 **ACM Transactions on Computer Systems (TOCS)**, Volume 14 Issue 1

Publisher: ACM Press

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